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Application Number	N/A
Filing Date	Herewith
First Named Inventor	David Muller, et al.
Art Unit	N/A
Examiner Name	N/A
Attorney Docket Number	MULLER 7-7

Sheet	1	of	2	Attorney Docket Number	MULLER 7-7
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## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner  
Signature**

Steph Stern

Date  
Considered

3/4/2004

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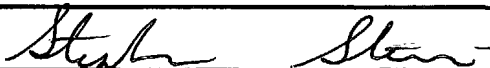
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Substitute for form 1449/PTO		<b>Complete if Known</b>			
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		Application Number	N/A		
		Filing Date	Herewith		
		First Named Inventor	David Muller, et al.		
		Art Unit	N/A		
		Examiner Name	N/A		
Sheet	2	of	2	Attorney Docket Number	MULLER 7-7

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
SSS		Kei-Ichi Yamaguchi, Shigen Imai, Naoto Ishitobi, Masashi Takemoto, Hidejiro Miki, and Masakiyo Matsumura; "ATOMIC-LAYER CHEMICAL-VAPOR-DEPOSITION OF SILICON DIOXIDE FILMS WITH AN EXTREMELY LOW HYDROGEN CONTENT"; June 1998, Applied Surface Science, Vol. 130-132, Pgs. 202-207.	
SSS		J.W. Klaus, O. Sneh, A.W. Ott and S.M. George; "ATOMIC LAYER DEPOSITION OF SiO <sub>2</sub> USING CATALYZED AND UNCATALYZED SELF-LIMITING SURFACE REACTIONS"; June-Aug. 1999; World Scientific, Surface Review and Letters, Vol. 6, Nos. 3 & 4, Pgs. 435-448.	
SSS		D.A. Muller, T. Sorsch, S. Moccio, F.H. Baumann, K. Evans-Lutterodt & G. Timp; "THE ELECTRONIC STRUCTURE AT THE ATOMIC SCALE OF ULTRATHIN GATE OXIDES"; 24 June 1999; Macmillan Magazines Ltd., Nature, Vol. 399; Pgs. 758-761.	
SSS		Leonello Dori, Alexandre Acovic, Donelli J. DiMaria and Ching, Hsiang Hsu; "OPTIMIZED SILICON-RICH OXIDE (SRO) DEPOSITION PROCESS FOR 5-V-ONLY FLASH EEPROM APPLICATIONS"; June 1993, IEEE Electron Device Letters, Vol. 14, No. 6; Pgs. 283-285.	
SSS		J.B. Neaton, D.A. Muller and N.W. Ashcroft; "ELECTRONIC PROPERTIES OF THE Si/SiO <sub>2</sub> INTERFACE FROM FIRST PRINCIPLES"; The American Physical Society, Volume 85, Number 6, 7 August 2002; Pgs. 1298-1301.	
SSS		David A. Muller, et al.; Serial No. 09/773,443 filed on October 2, 2003; "SILICON OXIDE BASED GATE DIELECTRIC LAYER".	

Examiner Signature		Date Considered	3/4/2004
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